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Fig.1A

(Prior Art)

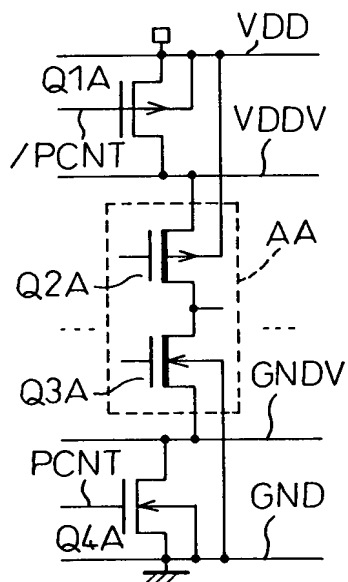


Fig.1B

(Prior Art)

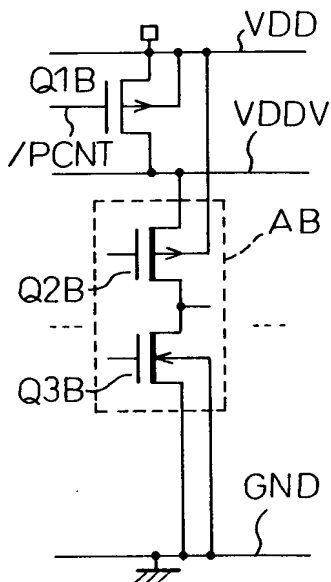


Fig.1C

(Prior Art)

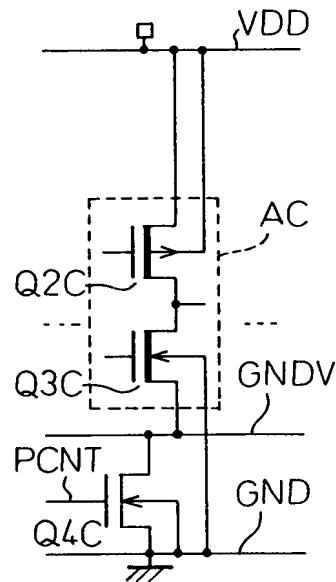


Fig.1D

(Prior Art)

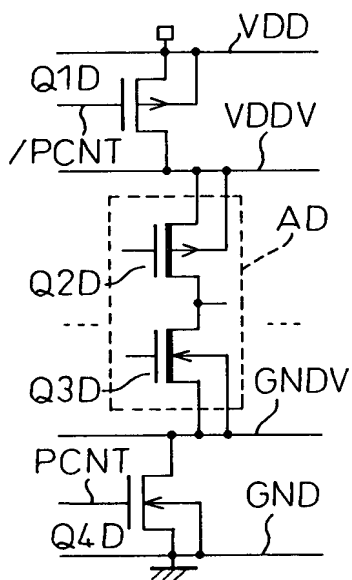


Fig.1E

(Prior Art)

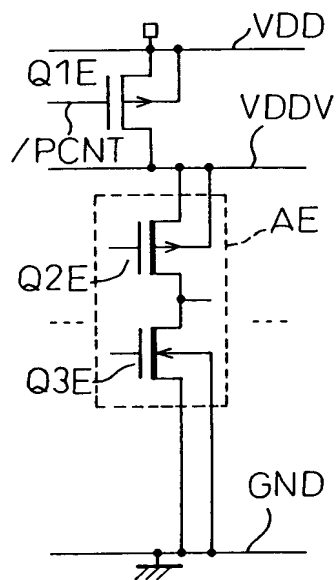
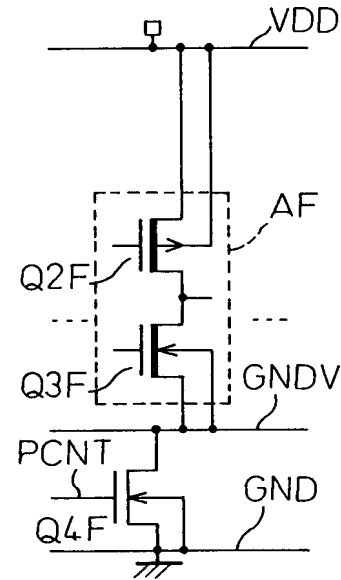


Fig.1F

(Prior Art)



High-V_{th} NMOSFET

Low-V_{th} PMOSFET

Low-V_{th} NMOSFET

High-V_{th} PMOSFET

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Fig. 2A

(Prior Art)

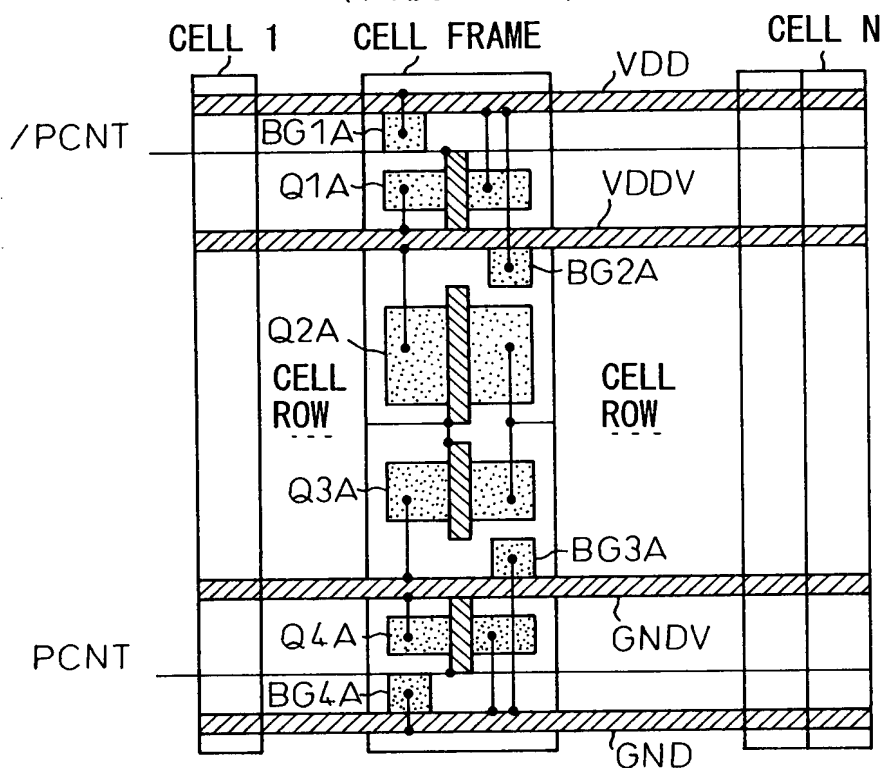
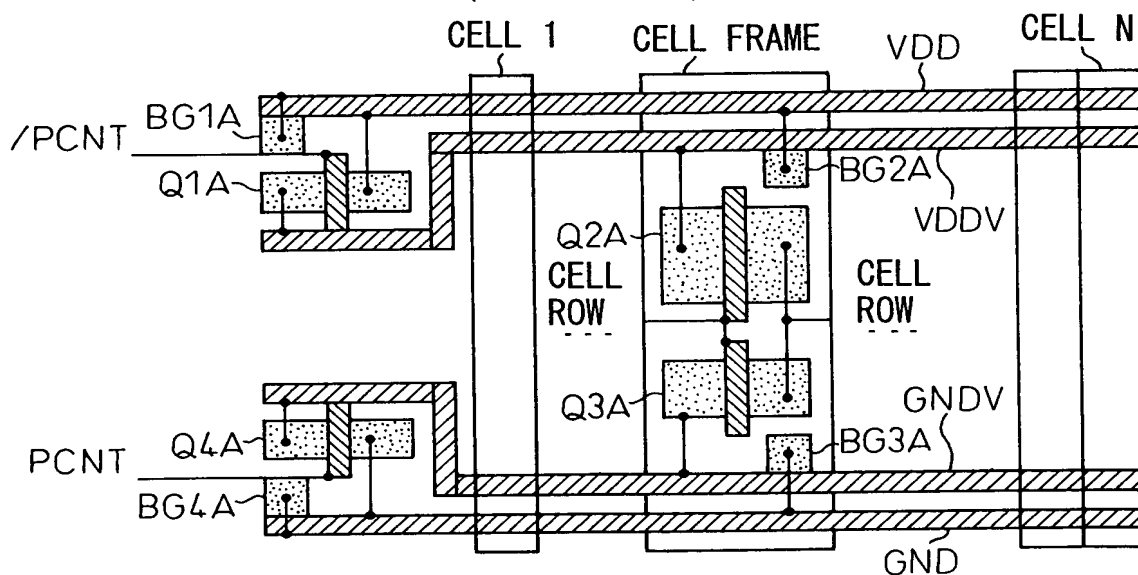


Fig. 2B

(Prior Art)



※ —•— INDICATES WIRING LINES
 AND THEIR CONNECTIONS

Fig. 3
 (Prior Art)

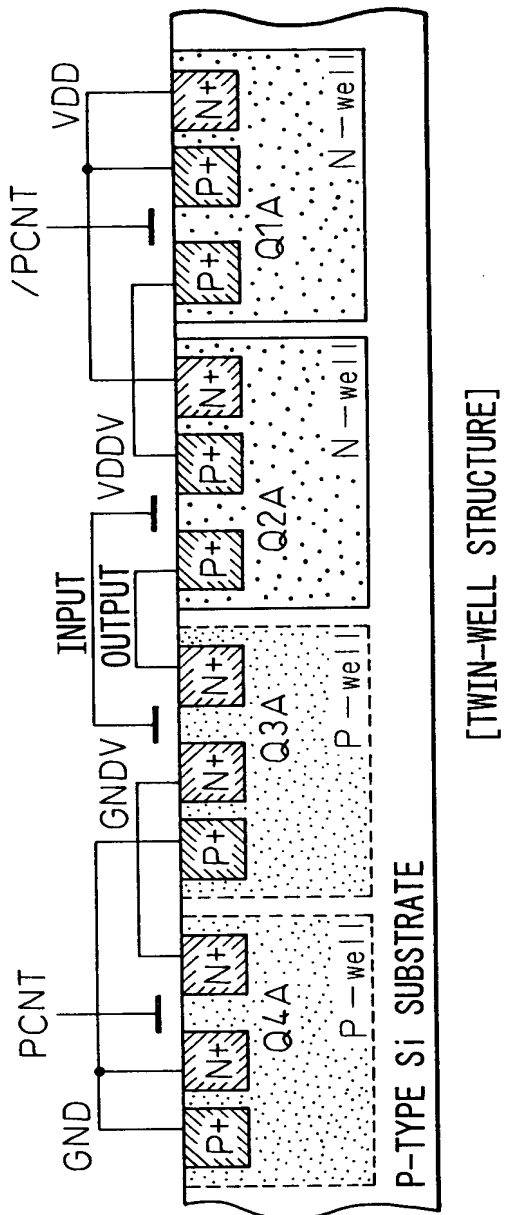
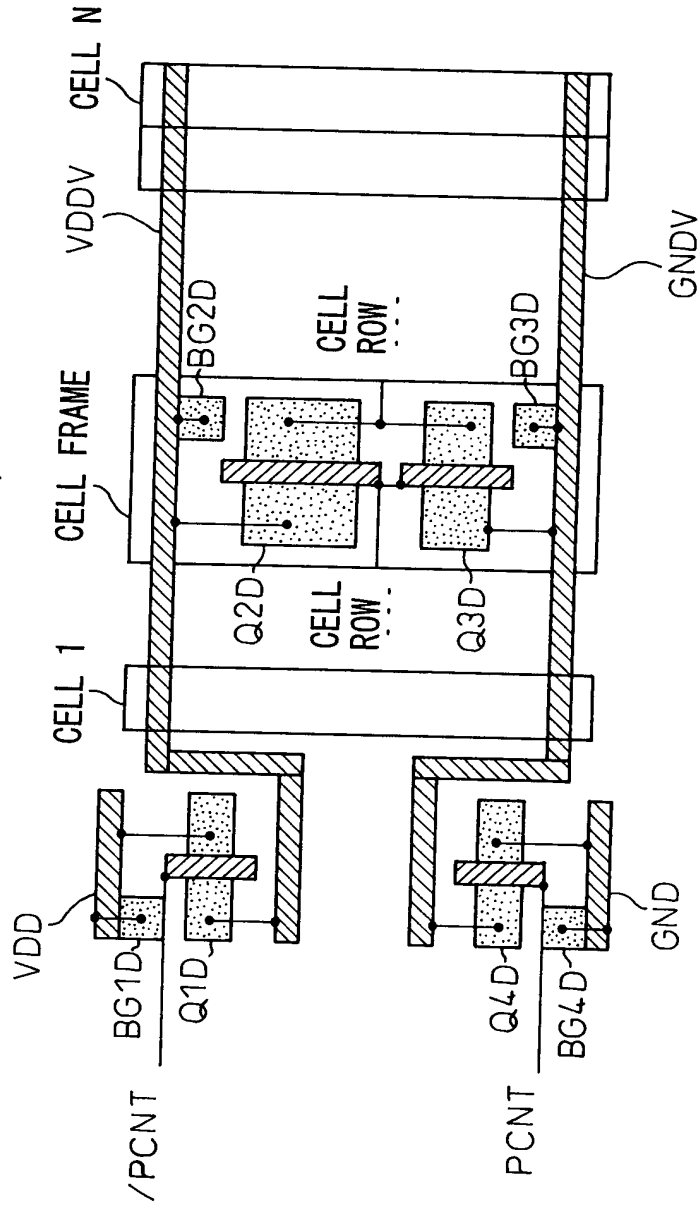


Fig. 4
 (Prior Art)



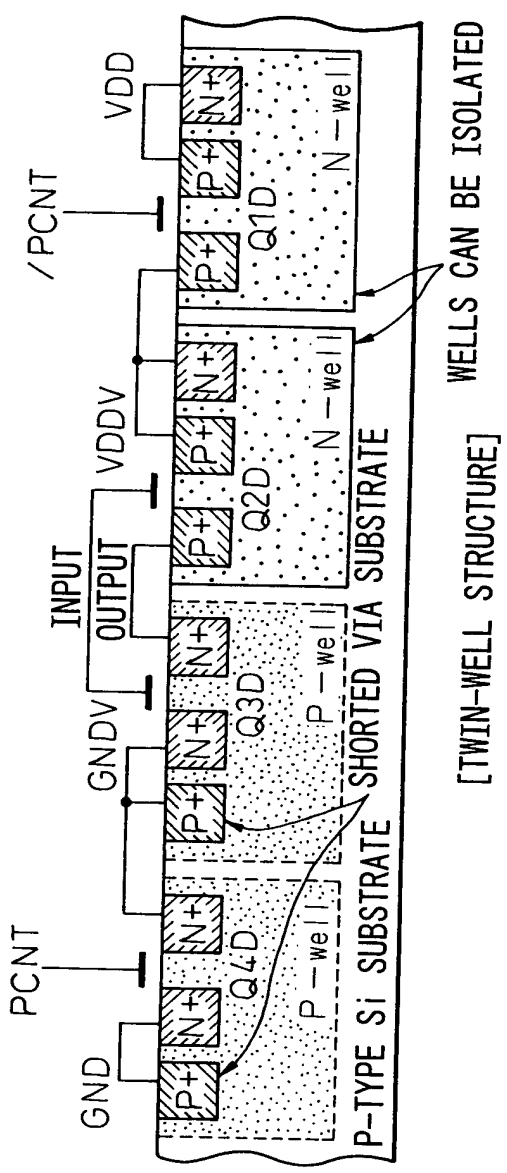


Fig. 5A
 (Prior Art)

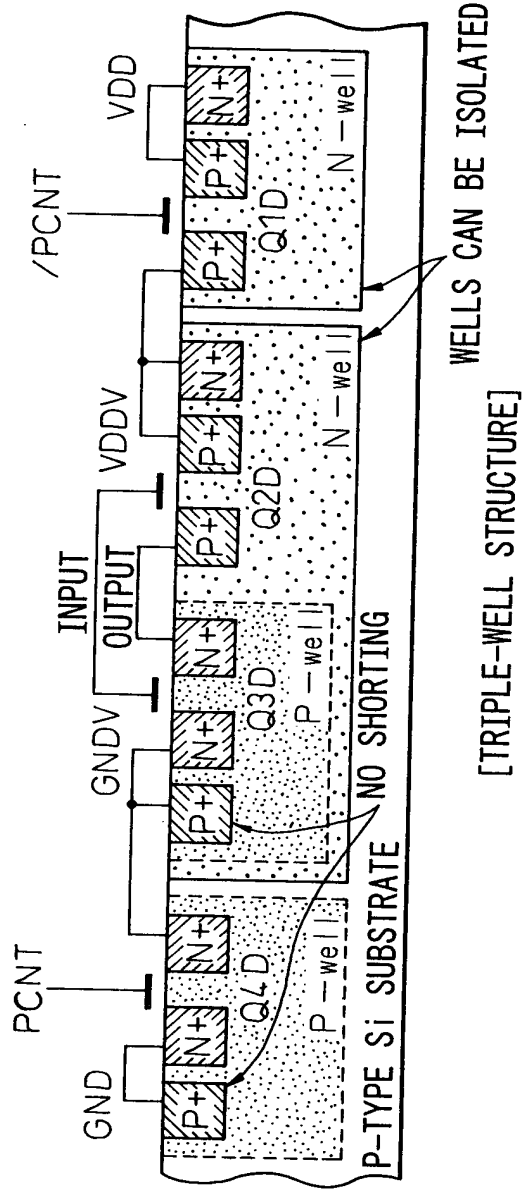
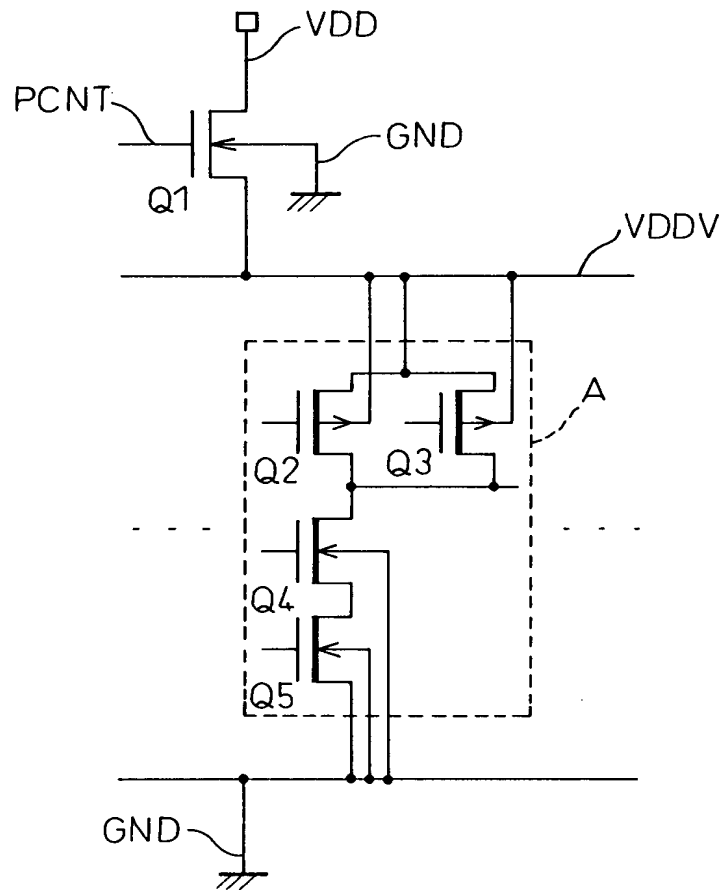


Fig. 5B
 (Prior Art)

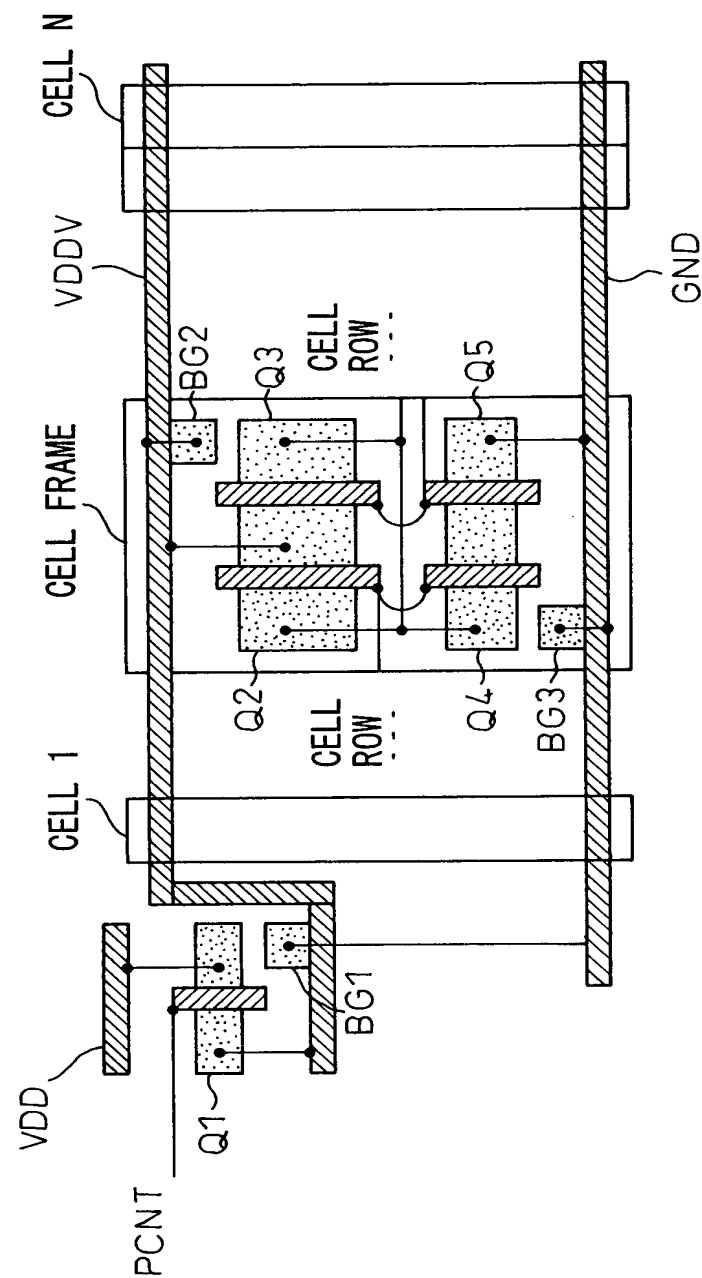
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Fig. 6



$\text{---} \text{H} \text{---} \text{H}$ High- V_{th} NMOSFET $\text{---} \text{H} \text{---} \text{H}$ Low- V_{th} PMOSFET
 $\text{---} \text{H} \text{---} \text{H}$ Low- V_{th} NMOSFET

Fig. 7



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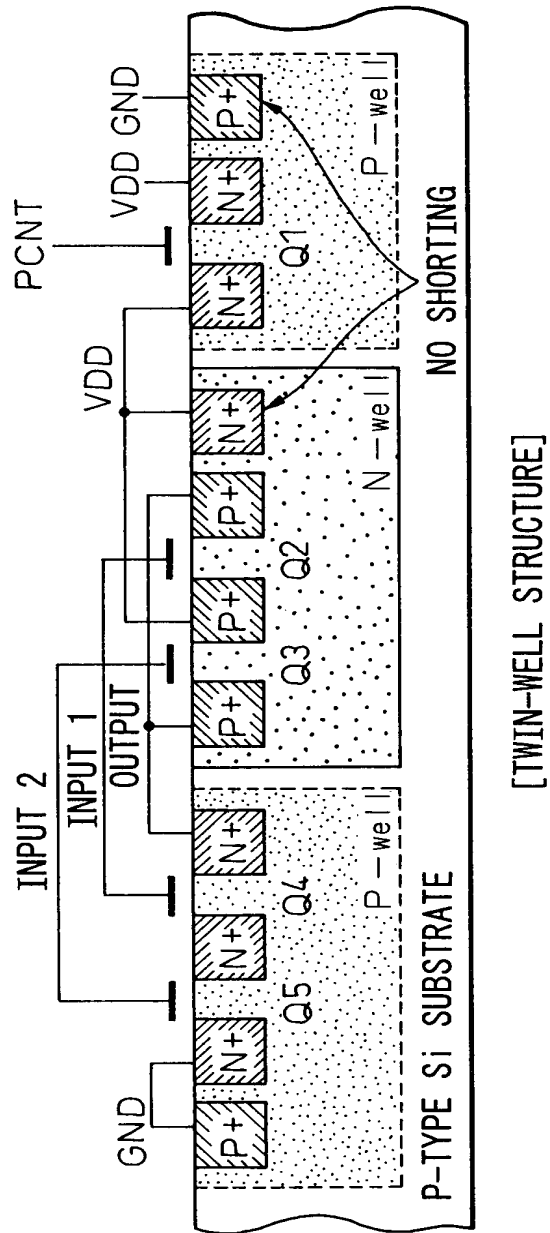


Fig. 9A

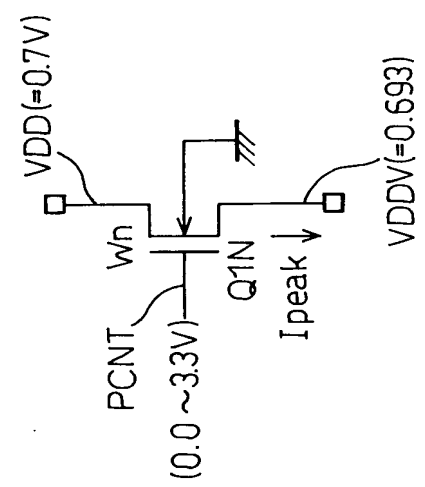


Fig. 9B

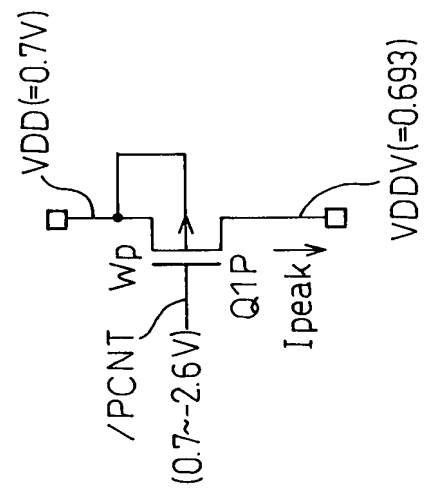
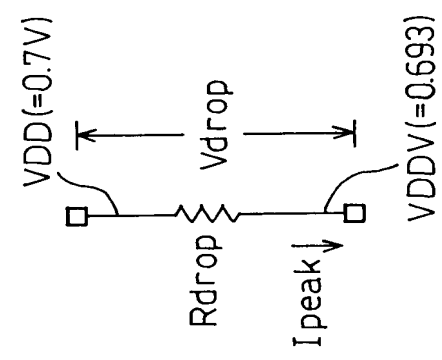


Fig. 9C



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Fig.10

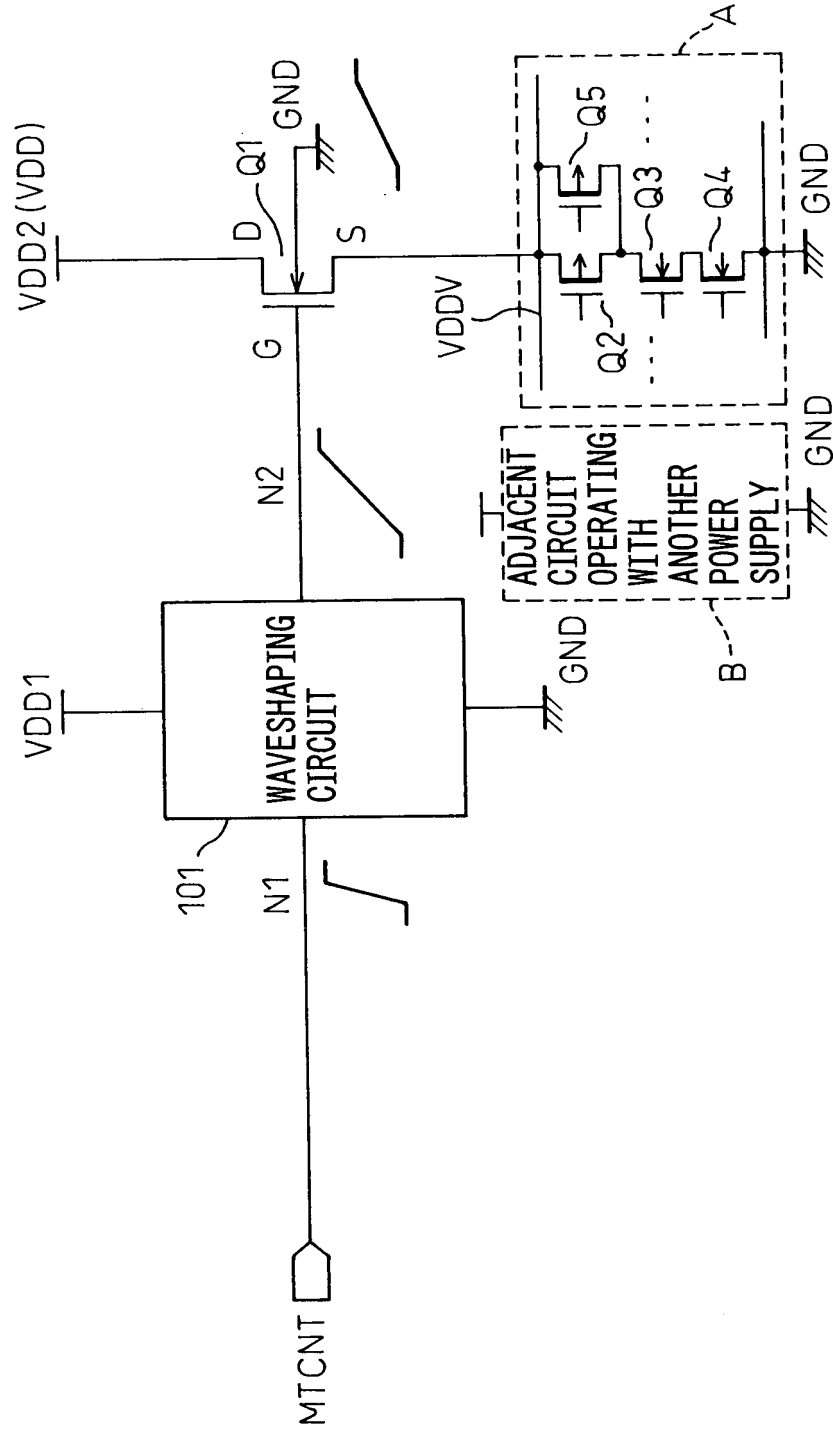


Fig. 11A

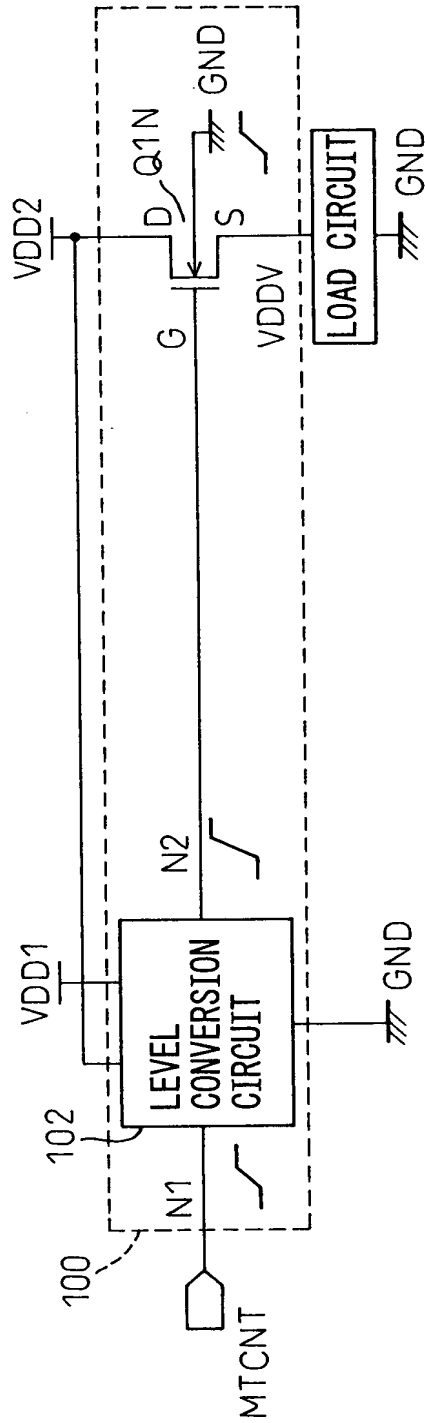


Fig. 11B

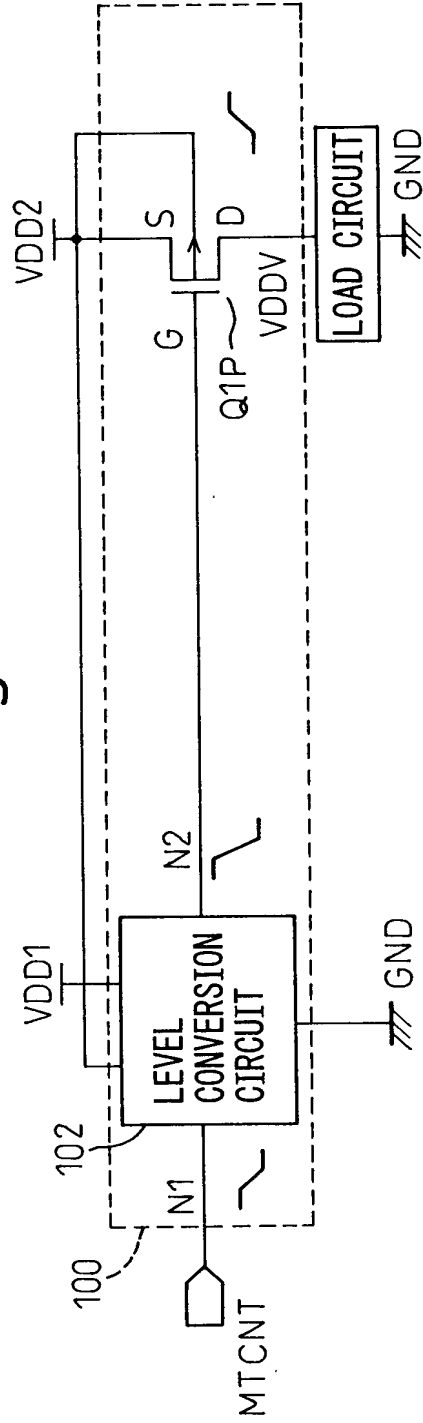
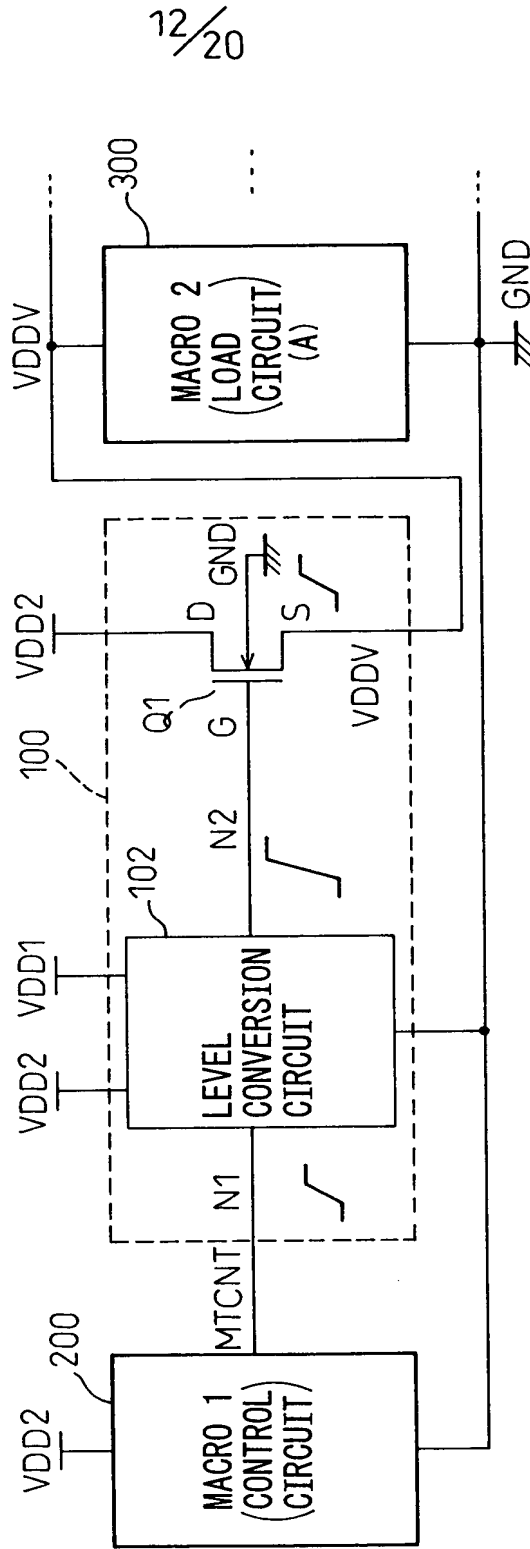


Fig.12



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Fig.13

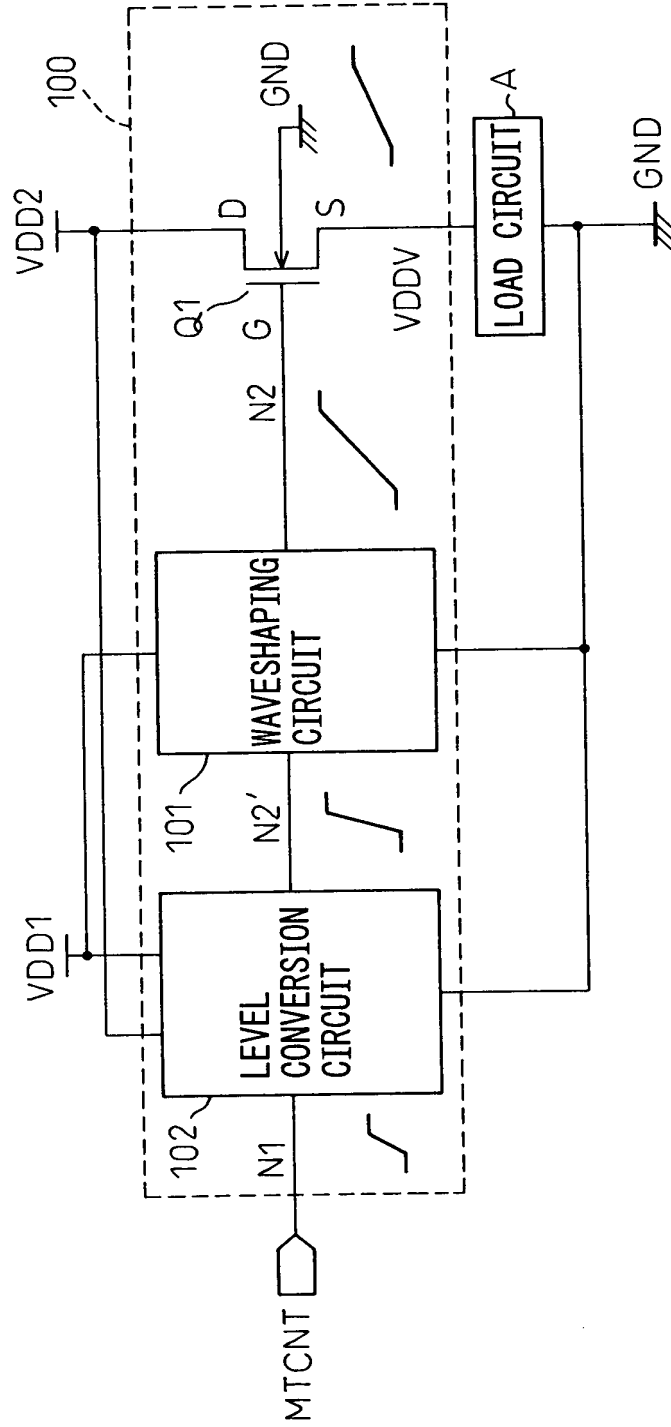


Fig.14

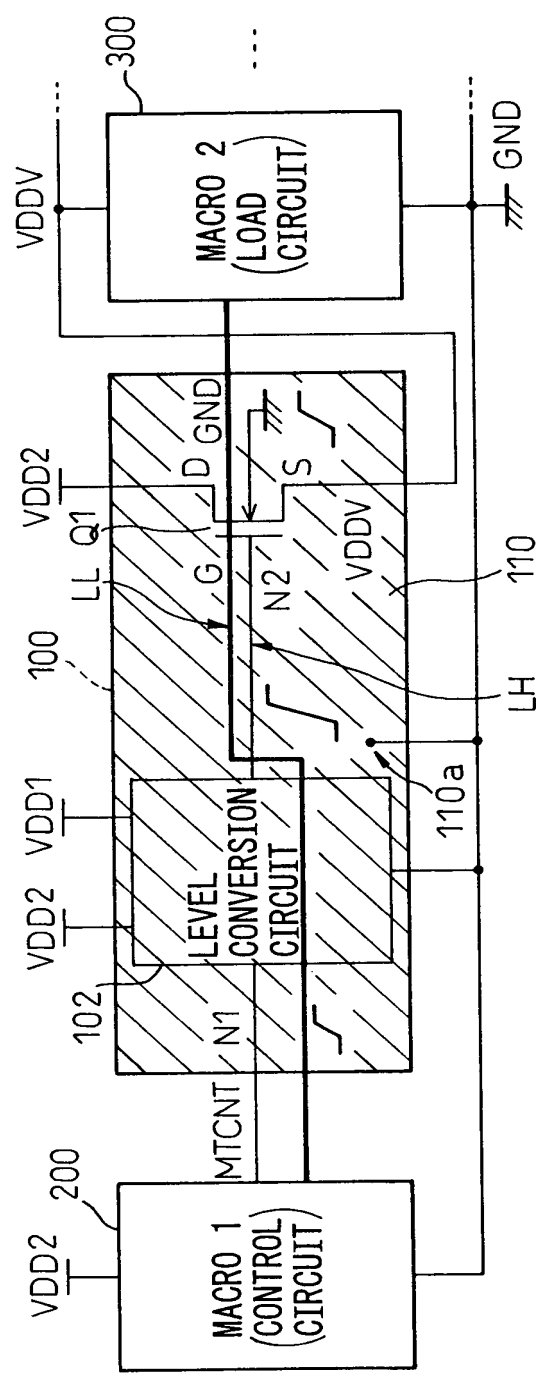


Fig.15

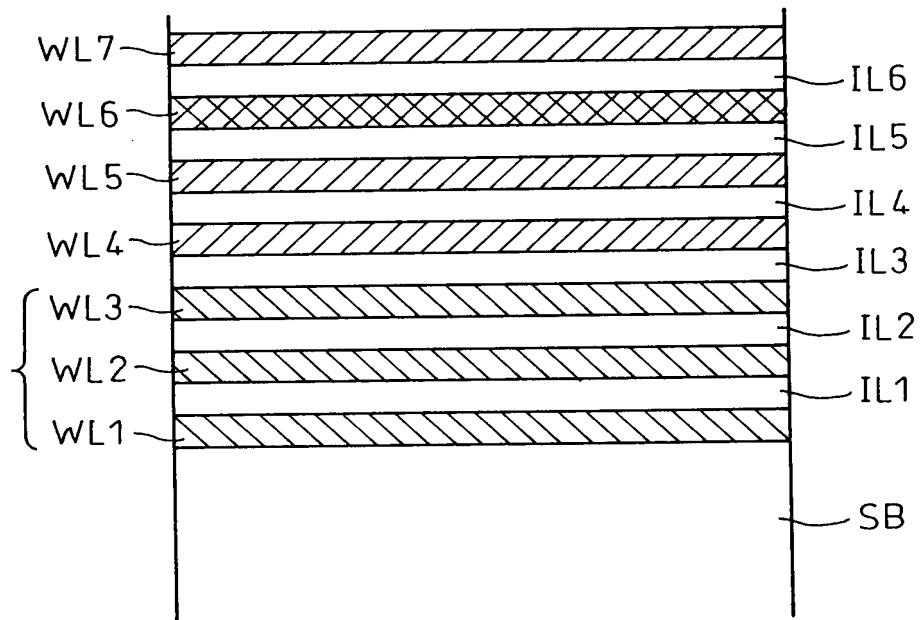
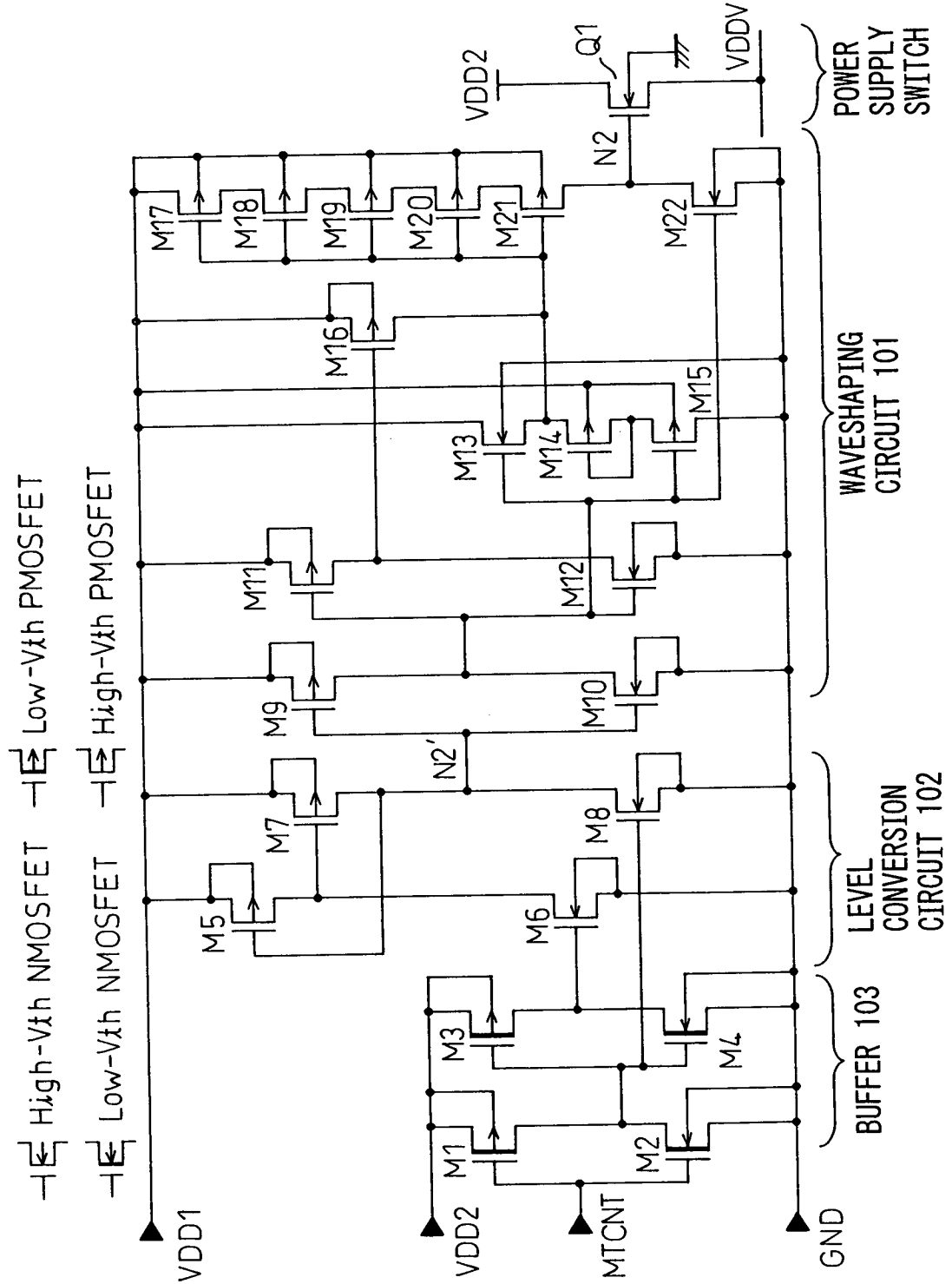
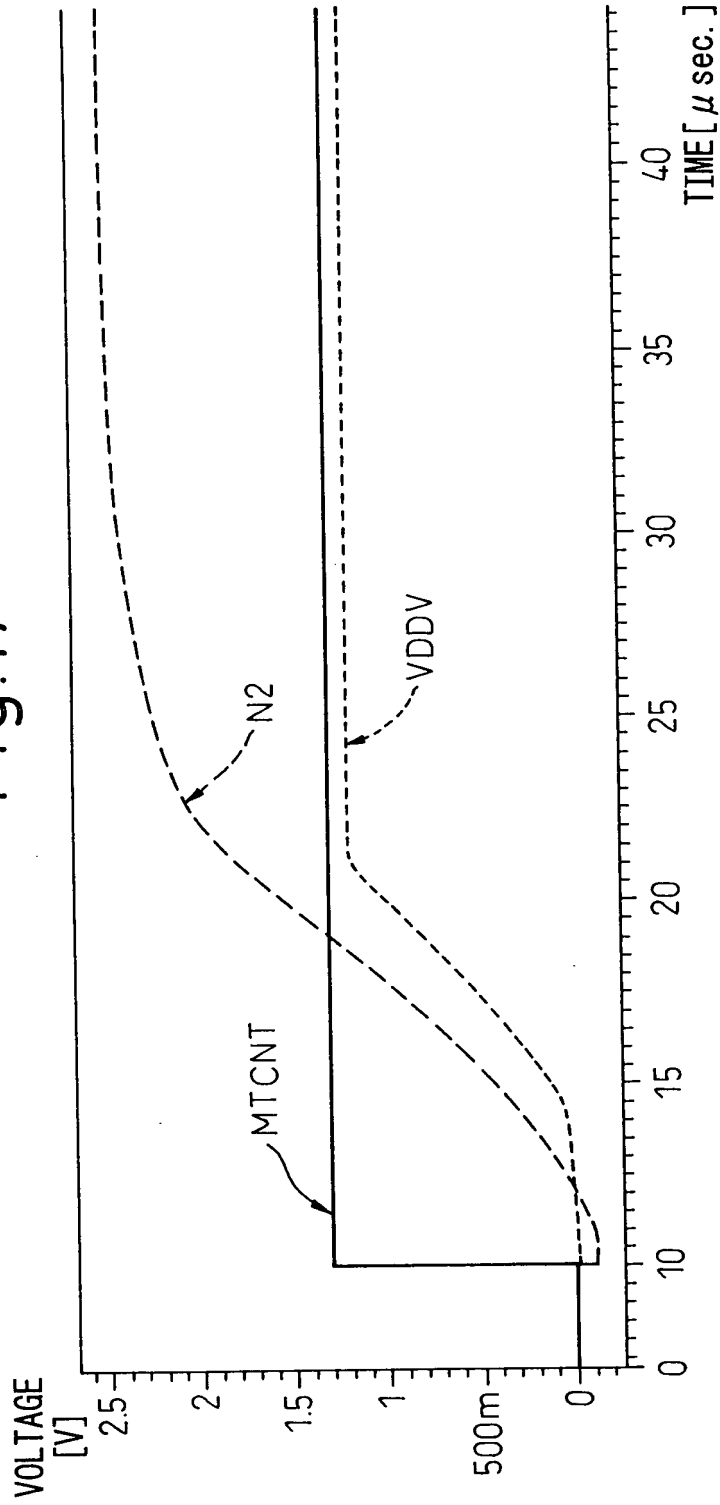


Fig.16



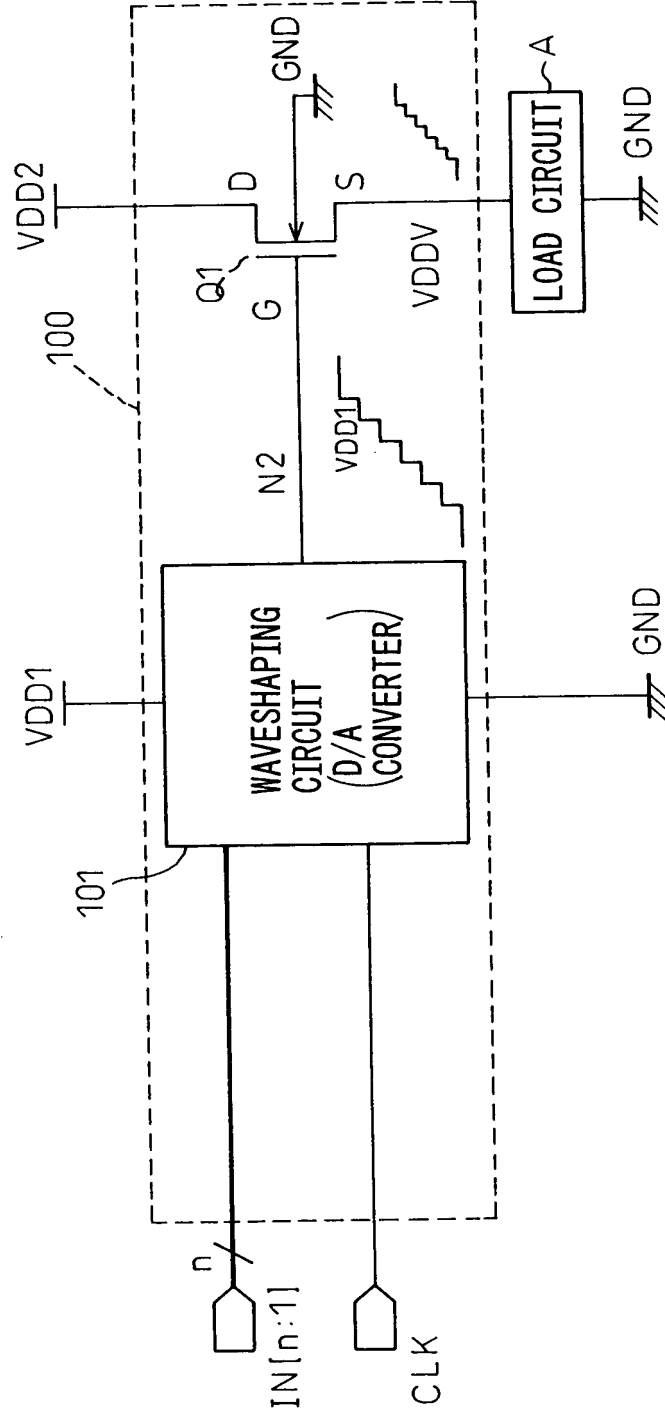
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Fig.17



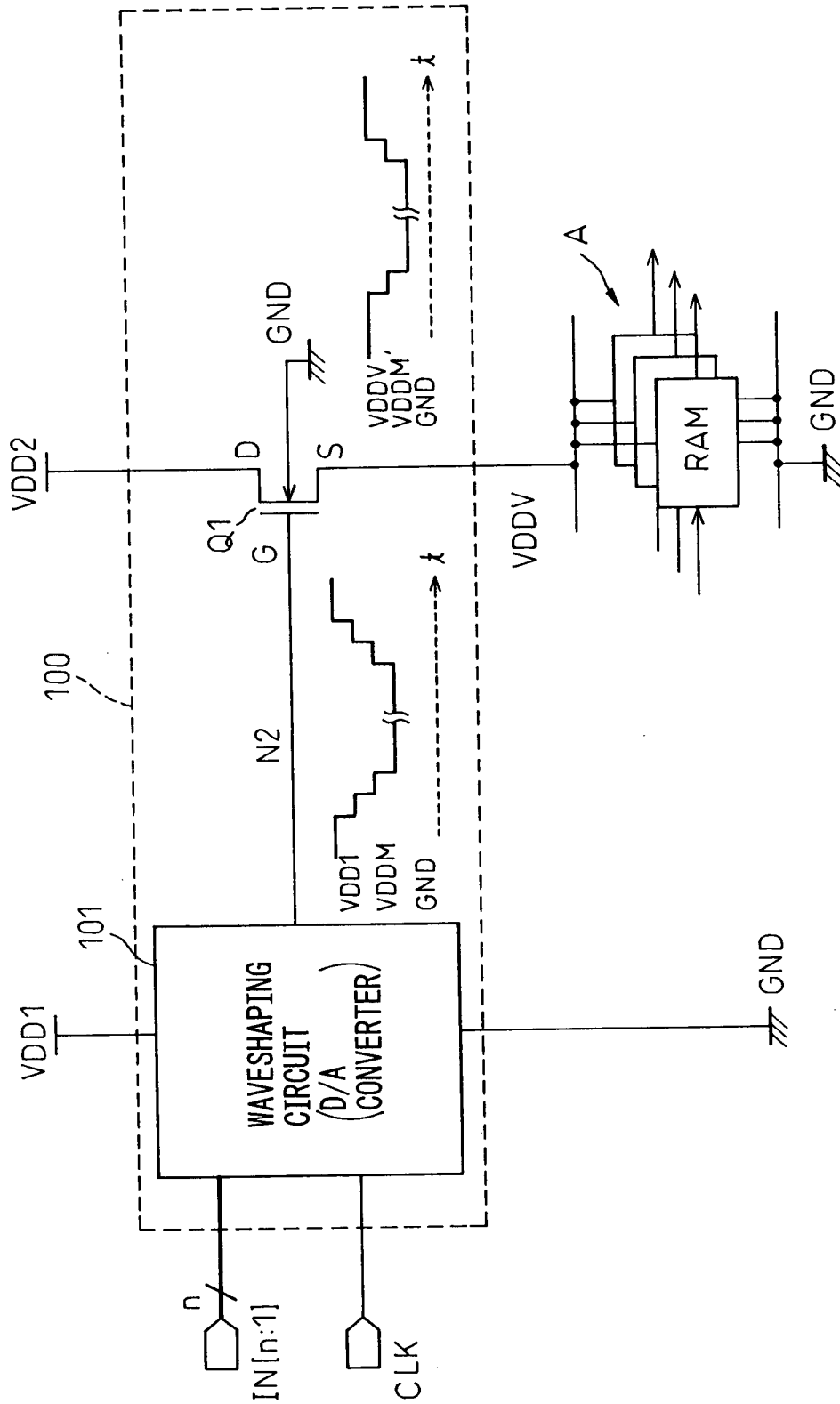
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Fig.18



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Fig.19



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Fig. 20

